

## ADC Ti ADC12J4000EVM

**Short description :** The ADC12J4000EVM is an evaluation module (EVM) that allows for the evaluation of Texas Instruments ADC12J4000. The ADC12J4000 is a low power, 12-bit, 4-GSPS RF-sampling analog to digital converter (ADC) with a buffered analog input, integrated Digital Down Converter with programmable NCO and Decimation settings (including un-decimated 12 bit ADC output) and features a JESD204B interface. The EVM has a transformer coupled analog input to accommodate a wide range of signal sources and frequencies. An LMX2581 clock synthesizer and LMK04828 JESD204B clock generator are included on the EVM and can be configured to provide an ultra-low-jitter ADC Device Clock and SYSREF for a complete JESD204B subclass 1 clocking solution.



ADC12J4000EVM Top View

The ADC12J4000, LMX2581 and LMK04828 are controlled through an easy to use software GUI to enable quick configuration for a variety of uses.

The ADC12J4000EVM connects directly to the TSW14J56EVM data capture hardware via the high speed FMC connector. The High Speed Data Converter Pro Software is also available for data capture and analysis support when using the TSW14J56EVM.

### Features

- Flexible transformer coupled analog input and clock input to allow for a variety of sources and frequencies
- Easy to use software GUI to configure the ADC12J4000, LMX2581 and LMK04828 for a variety of configurations through a USB interface
- Quickly evaluate ADC performance through High Speed Data Converter Pro software
- Simple connection to TSW14J56EVM data capture card.

**Localisation :** Maxime Drouguet

**Personne(s) ressource(s) :** Maxime Drouguet,

**Site Web :** [www.uclouvain.be/welcome](http://www.uclouvain.be/welcome)

**Contact :** [info-welcome@uclouvain.be](mailto:info-welcome@uclouvain.be)