





5.00 credits

30.0 h + 30.0 h

Q1

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| Teacher(s) | Flandre Denis ;Legat Jean-Didier ; |
| Language : | French |
| Place of the course | Louvain-la-Neuve |
| Prerequisites | <i>The prerequisite(s) for this Teaching Unit (Unité d'enseignement – UE) for the programmes/courses that offer this Teaching Unit are specified at the end of this sheet.</i> |
| Learning outcomes | |
| Evaluation methods | An oral or written exam (depending on the session) will be organized, in addition to a possible ongoing evaluation. Details are defined on the course website. |
| Inline resources | http://moodleucl.uclouvain.be/course/view.php?id=76 |
| Bibliography | - Notes de cours sur le site Moodle - Microelectronic Circuits by Sedra/Smith - Oxford University Press - CMOS Circuit Design, Layout, and Simulation, Third Edition - R. Jacob Baker - Wiley-IEEE Press |
| Faculty or entity in charge | ELEC |

| Programmes containing this learning unit (UE) | | | | |
|---|---------------------------|---------|--------------|---|
| Program title | Acronym | Credits | Prerequisite | Learning outcomes |
| Master [120] in Mechanical Engineering | MECA2M | 5 | |  |
| Minor in Engineering Sciences: Electricity (only available for reenrolment) | MINELEC | 5 | |  |
| Specialization track in Electricity | FILELEC | 5 | |  |
| Minor in Electricity | LMINOELEC | 5 | |  |