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| 5.0 credits | 75.0 h | 1 + 2q |
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| Teacher(s) : | Louveaux Jérôme ; Vandendorpe Luc ; Legat Jean-Didier ; |
| Language : | Anglais |
| Place of the course | Louvain-la-Neuve |
| Prerequisites : | Students are expected to master the following skills: design and architecture of digital electronic systems including Verilog (or VHDL) and FPGA , as they are covered within the courses LELEC2531 |
| Aims : | <i>The contribution of this Teaching Unit to the development and command of the skills and learning outcomes of the programme(s) can be accessed at the end of this sheet, in the section entitled "Programmes/courses offering this Teaching Unit".</i> |
| Faculty or entity in charge: | ELEC |

| Programmes / formations proposant cette unité d'enseignement (UE) | | | | |
|--|--------|---------|-----------|---|
| Intitulé du programme | Sigle | Credits | Prerequis | Acquis d'apprentissage |
| Master [120] in Electrical Engineering | ELEC2M | 5 | - |  |
| Master [120] in Electro-mechanical Engineering | ELME2M | 5 | - |  |