

LELEC2531

2013-2014

Design and Architecture of digital electronic systems

5.0 credits 30.0 h + 30.0 h 1q

Teacher(s) :	Legat Jean-Didier ;
Language :	Français
Place of the course	Louvain-la-Neuve
Inline resources:	Moodle > http://moodleucl.uclouvain.be/enrol/index.php?id=4
Prerequisites :	No prerequisites
Aims:	At the end of this course, the students will be able to: Understand how the digital circuits (combinational circuits, sequential circuits) work Understand the architecture of programmable circuits (FPGA) Synthesize and simulate digital circuits in a language such as Verilog or VHDL Understand the architecture of a RISC processor Use and program a microcontroller Understand and implement a digital electronic system The contribution of this Teaching Unit to the development and command of the skills and learning outcomes of the programme(s) can be accessed at the end of this sheet, in the section entitled "Programmes/courses offering this Teaching Unit".
Teaching methods :	Learning is based on courses with compulsory homework Each student has at his disposal during the semester an electronic system comprising an FPGA (Altera Cyclone IV) and and PIC32 microcontroller from Microchip This course is closely linked to the project LELEC2103: Electronic System
Content:	Combinational logic Sequential logic Implementation technology Simulation language and Verilog synthesis Main logic circuits: arithmetic circuits, memories, programmable circuits Architecture and microarchitecture of a RISC processor Memories (caches,) Architecture of microcontrollers Peripherals and main communication systems
Bibliography :	Digital Design and Computer Architecture - David Money Harris @ Sarah L. Harris - 2007, Elsevier
Cycle and year of study:	Master [120] in Biomedical Engineering Master [120] in Electro-mechanical Engineering Master [120] in Electrical Engineering Master [120] in Electrical Engineering

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Faculty or entity in	ELEC
charge:	