

5.0 credits	30.0 h + 30.0 h	1q
-------------	-----------------	----

Teacher(s) :	Legat Jean-Didier ;
Language :	Français
Place of the course	Louvain-la-Neuve
Main themes :	Identical to the contents of the course
Aims :	<p>The aim of the course is to study in-depth advanced digital integrated circuits and digital electronics systems. This course will also introduce reconfigurable architectures and parallel processor architectures.</p> <p><i>The contribution of this Teaching Unit to the development and command of the skills and learning outcomes of the programme(s) can be accessed at the end of this sheet, in the section entitled "Programmes/courses offering this Teaching Unit".</i></p>
Content :	<p>1) Advanced digital integrated circuits</p> <ul style="list-style-type: none"> <li>- precharged circuits (Domino Logic, No-Race, TSPC)</li> <li>- differentials circuits</li> <li>- arithmetic circuits (adders, multipliers, PLA)</li> <li>- test of digital circuits</li> </ul> <p>2) Reconfigurable architectures</p> <ul style="list-style-type: none"> <li>- PLD, CPLD, FPGA</li> <li>- hardware-software codesign</li> <li>- simulation and synthesis : VHDL and SystemC</li> </ul> <p>3) Processor architectures</p> <ul style="list-style-type: none"> <li>- risk and pipelined architectures</li> <li>- parallel architectures (VLIW, SIMD, superscalar)</li> <li>-DSP architectures</li> </ul>
Other infos :	<p>Prerequisite :</p> <p>ELEC2531 : Electronics III</p> <p>Assessment method :</p> <p>Works performed during the semester Based on small projects done by the students (VHDL, SystemC, Eldo simulations) on a written examination</p> <p>Support :</p> <p>Copy of the slides and a dedicated website</p>
Cycle and year of study :	<p><a href="#">&gt; Master [120] in Electrical Engineering</a></p> <p><a href="#">&gt; Master [120] in Electro-mechanical Engineering</a></p>
Faculty or entity in charge:	ELEC