

5.0 credits	30.0 h + 30.0 h	2q
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Teacher(s) :	Legat Jean-Didier ; Flandre Denis ;
Language :	Français
Place of the course	Louvain-la-Neuve
Main themes :	Identical to the contents of the course
Aims :	<p>During this activity, the students will be given the opportunity</p> <ul style="list-style-type: none"> <li>- to discover the main classes of application electronic circuits such as operational amplifiers, voltage references, A/D and D/A converters, oscillators, mixers, phase locked loops, etc.</li> <li>- to analyse the architecture, to understand the behaviour, and to determine, to compute and to simulate the characteristics of these circuits</li> </ul> <p><i>The contribution of this Teaching Unit to the development and command of the skills and learning outcomes of the programme(s) can be accessed at the end of this sheet, in the section entitled "Programmes/courses offering this Teaching Unit".</i></p>
Content :	<p>General purpose analog circuits                      CMOS operational amplifier                      Output stages                      Signal generation                      Noise                      D/A and A/D converters</p> <p>Telecommunication circuits                      Active                      Oscillators                      Mixers                      Phase locked loops</p>
Other infos :	<p>Teaching and learning methods :                      Lectures, exercises</p> <p>Prerequisites :                      ELEC1530 : Electronics I</p> <p>Assessment :                      Oral examination</p> <p>References :                      Slides on : <a href="http://www.icampus.ucl.ac.be/">http://www.icampus.ucl.ac.be/</a>                      This course is often referring to : Analysis and design of analog integrated circuits, Gray, Hurst, Lewis and Meyer, John Wiley 2001</p>
Cycle and year of study :	<p>&gt; <a href="#">Master [120] in Electrical Engineering</a>                      &gt; <a href="#">Master [120] in Electro-mechanical Engineering</a></p>
Faculty or entity in charge:	ELEC