

5.0 credits	75.0 h	1+2q
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Teacher(s) :	Legat Jean-Didier ; Sobieski Piotr ; Vandendorpe Luc ;
Language :	Français
Place of the course	Louvain-la-Neuve
Main themes :	The project consists in designing, implementing on an appropriate hardware, simulating and testing a telecommunication system
Aims :	After this course the students will be able to : - design, simulate and test a telecommunication system - design, simulate and test an electronic system based on FPGA, a microcontroller and an analog part used for implementing the telecommunication system <i>The contribution of this Teaching Unit to the development and command of the skills and learning outcomes of the programme(s) can be accessed at the end of this sheet, in the section entitled "Programmes/courses offering this Teaching Unit".</i>
Content :	Teaching methods : Design a telecommunication system on the basis of given specifications Modelling and simulation of the system Design, synthesis and simulation of a digital electronic system including FSM, counters, registers, interface Implementation of the system in a FPGA Use and programming of a microcontroller Design and spice simulation of the analog part of the telecommunication system Realization and test of the whole system
Other infos :	Prerequisites : Students take simultaneously the course ELEC2531 "Electronique II : circuits électroniques digitaux", and : - the course ELEC2795 "Rayonnement et systèmes de télécommunication" if they are in the Master in electrical engineering, - the course ELEC2660 "Electronique de puissance", if they are in the Master in electromechanical engineering, option mechatronics. These courses are taught in parallel with the project and in close cooperation with it. Assessment : The evaluation will be based on various elements : the work during the year, the final demonstration at the end of the project, the final report, the final presentation. No other presentation during the examination periods. Observation : This project is carried out by groups of 3 to 4 students
Cycle and year of study :	> Master [120] in Electrical Engineering > Master [120] in Electro-mechanical Engineering > Master [120] in Biomedical Engineering
Faculty or entity in charge:	ELEC